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Hogan & Hartson, Tokyo 813-59084071

NO. 8434 P. 11/106



PATENT
81788.0163

RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2187

#27

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Art Unit 2759

Yoshiyuki TANAKA, et al.

Examiner: D. Robertson

Serial No.: 09/130,818

Filed: August 7, 1998

For: METHOD FOR CONTROLLING NON-VOLATILE
SEMICONDUCTOR MEMORY SYSTEM BY
USING LOOK UP TABLE (As Amended)

REQUEST FOR EXTENSION OF TIME

Assistant Commissioner for Patents
Washington, D.C. 20231
Attention: Box AF

I hereby certify that this correspondence is
being facsimile transmitted to the United
States Patent and Trademark Office, Fax No.
703 746-7239 on November 24, 2002.

Sterlon R. Mason Reg. No. 41,179
Name _____
Signature _____ Date November 24, 2002

Dear Sir:

In accordance with 37 C.F.R. § 1.116, Applicants respectfully petition the Commissioner
for a one-month extension of time extending October 26, 2002 to November 26, 2002, the period
for response to the Office Action dated July 26, 2002. The responsive papers are attached.

08/28/2003 LKINARD 00000001 09130818 Please charge the fee of \$110.00 and any insufficiency or credit any overpayment to
01 FC:1251 Deposit Account No. 50-1314.

Respectfully submitted,

HOGAN & HARTSON, L.L.P.

By:

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Date: November 24, 2002

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